FEE TRANSMITTAL

Electronic Version v08

Stylesheet Version v08.0

Title of Invention

Reducing Coupling Effect on Reference Voltages When Output Buffers Implemented with Low Voltage Transistors Generate High Voltage Output Signals

Application Number:

Date:

First Named Applicant: Mr. Rajat CHAUHAN

Attorney Docket Number: TI-38154

TOTAL FEE AUTHORIZED \$ 856

Patent fees are subject to annual revisions on or about October 1st of each year.

Filing as large entity

BASIC FILING FEE

| Fee Description | Fee Code | Amount \$ | Fee Paid \$ | | | |
|--|----------|-----------|-------------|--|--|--|
| Utility Filing Fee | 1001 | 770 | 770 | | | |
| Subtotal For Basic Filing Fees: \$ 770 | | | | | | |

EXTRA CLAIM FEES

| Fee Description | Extra Claim | Fee Code | Amount \$ | Fee Paid \$ | | |
|---------------------------------------|-------------|----------|-----------|-------------|--|--|
| Total Claims : 20 | 0 | 1202 | 18 | 0 | | |
| Independent Claims: 04 | 1 | 1201 | 86 | 86 | | |
| Subtotal For Extra Claims Fees: \$ 86 | | | | | | |

AUTHORIZED BILLING INFORMATION

The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit account number: 200668

Deposit name: Texas Instruments Incorporated

Deposit authorized name: Narendra R. Thappeta

Signature: Naren Thappeta

Date (YYYYMMDD): 2004-08-10

Charge Assignment Fees Required Under 37 C.F.R. Section 1.21 (h).

Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.